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voltages. Some interface devices in digital logic require both positive and negative polarity power supplies, and in those circuits, it is common to see a 0V ground reference. Turn off the trainer for the next measurement.

3. Variable Voltage Supply 3.1.

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Laboratory Manual

Lab Manual: Digital Electronics

Lab (EE-224-F) DEPARTMENT

OF ELECTRONICS &

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Page 12 An OR gate can have two

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For more inputs, its output is true if at least one input is true. NOT Gate (Inverter) Input A Output Q 0 1 1 0 Traditional symbol Truth Table It is also known as inverter.

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PROCEDURE: (a) With given
equation in SOP/POS forms first of
all draw a K-map. (b) Enter the
values of the O/P variable in each
cell corresponding to its Min/Max

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Term. (c) Make group of adjacent ones. (d) From group write the minimized equation. (e) Design the ckt. of minimized equation & verify the truth table.

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Keying: Generation and Detection.
6. Phase Shift Keying: Generation
and Detection. 7.

DIGITAL COMMUNICATIONS

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This lab manual provides an introduction to digital logic, starting with simple gates and building up to state machines. Students should have a solid understanding of algebra as well as a rudimentary understanding of

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Basic electricity including voltage, current, resistance, capacitance, inductance and how they relate to direct current

Introduction to Digital Logic with
Laboratory Exercises

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COMMUNICATION ENGINEERING
DIGITAL ELECTRONICS
LABORATORY LAB MANUAL –
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**2016-2017 Prepared by: Reviewed
by: Approved by: Mrs. A. Deepa
Mrs. Kavitha M V Dr. A.A. Powly**

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Thomas Assistant Professor Head
of the Department Principal

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